## **ELECTRONIC INFORMATION DISCLOSURE STATEMENT**

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

LADDER-TYPE GATE STRUCTURE FOR FOUR-TERMINAL SOI SEMICONDUCTOR DEVICE

**Application Number:** 

Confirmation Number:

First Named Applicant:

Paul Hyde

Attorney Docket Number:

BUR920030094US1

Art Unit:

Examiner:

Search string:

( 6436744 or 6249477 or 6307237 or 6275059 or 5930605 or 5770881 or 5215931

or 4983535 or 20030064567 or 20020106844 or 20020053706 ).pn

## **US Patent Documents**

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
ML	1	6436744	2002-08-20	Bryant et al.			
WL	2	6249477	2002-08-06	Mandelman et al.			
W	3	6307237	2001-10-23	Erstad			
W	4	6275059	2001-08-14	Sah et al.			
WL	5	5930605	1999-07-27	Mistry et al.			
WC	6	5770881	1998-06-23	Pelella et al.			
WL	7	5215931	1993-06-01	Houston			
W	8	4983535	1991-01-08	Blanchard			

## **US Published Applications**

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
ML	1	20030064567	2003-04-03	Chaudhry et al.			
WL	2	20020106844	2002-08-08	Kotani		!	
WC	3	20020053706	2002-05-09	Jung et al.		_	

## **Signature**

Examiner Name	Date		
Walk Lewith	10/27/2004		

10/27/2004